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APPLICANTS

Seong-wook Kim, Seoul, KOREA, REPUBLIC OF;

** CONTINUING DATA ***** ^{NONE PD}

** FOREIGN APPLICATIONS ***** ^{YES PD}
 REPUBLIC OF KOREA 2003-05142 01/27/2003

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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <u>Seong-wook Kim</u> ^{PD} Examiner's Signature Initials	STATE OR COUNTRY KOREA, REPUBLIC OF	SHEETS DRAWING 2	TOTAL CLAIMS 6	INDEPENDENT CLAIMS 2
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TITLE

Method for fabricating merged logic CMOS device

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